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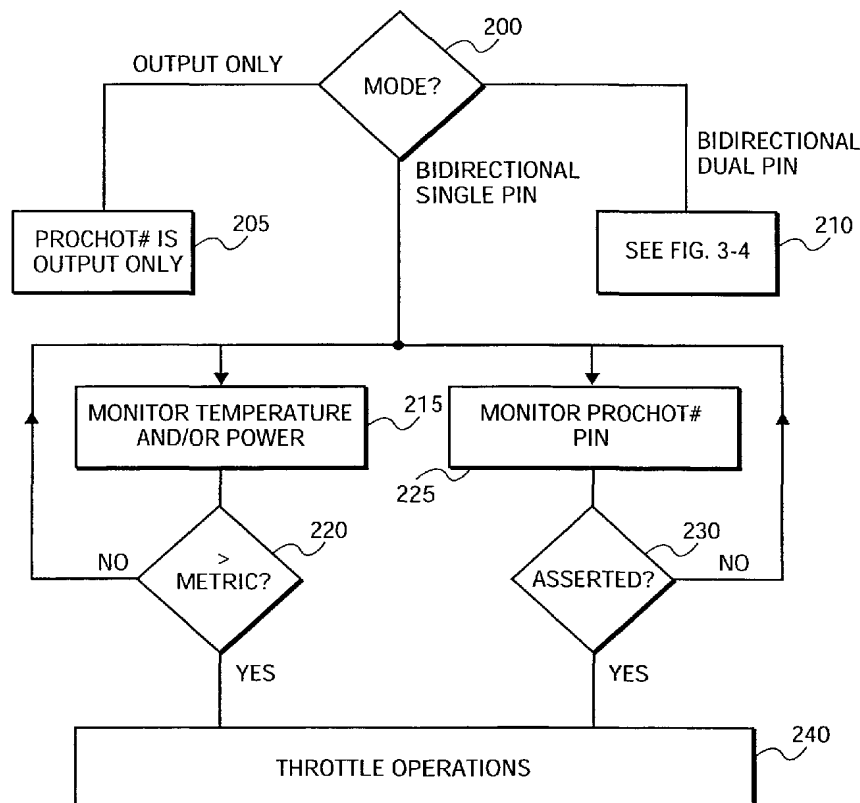
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(54) Title: PROCESSOR TEMPERATURE CONTROL INTERFACE



(57) Abstract: Techniques for a processor temperature control interface. In one embodiment, a processor includes a bidirectional interface and output logic to assert a first signal indicating an internal high temperature on the bidirectional interface. Throttling logic throttles operations of the processor if either the internal high temperature is indicated or if an external signal is received on the bidirectional interface.



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